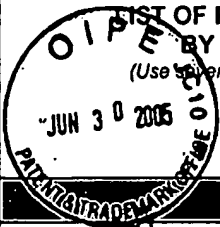


Form PTO-1449		ATTORNEY DOCKET NO. AUS920040061US1		SERIAL NO. 10/806,576	
 LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT <b>Dimpsey et al.</b>			
		FILING DATE <b>March 22, 2004</b>		GROUP ART UNIT <b>2188</b>	
<b>U.S. PATENT DOCUMENTS</b>					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
	<del>AA 6,103,304</del>	<del>Apr. 7, 1992</del>	<del>Blasziak</del>	<del>395/575</del>	<del>Dec. 21, 1989</del>
	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999
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DATE CONSIDERED		10/23/2006		EXAMINER /Craig Walter/	
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FILING DATE <b>March 22, 2004</b>				GROUP ART UNIT <b>2188</b>	
<b>U.S. PATENT DOCUMENTS</b>					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
	<del>BB 2002/0120300</del>	<del>Sep. 12, 2002</del>	<del>Floyd et al.</del>	<del>744/724</del>	<del>Dec. 10, 2000</del>
	BC 2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
	BD 2002/0199179	Dec. 26, 2002	Lavery et al.	717/158	Jun. 21, 2001
	BE 2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29, 2000
	BF 2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2001
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<b>FOREIGN PATENT DOCUMENTS</b>					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
	BH JP2000029731	Dec. 8, 1999	Japan	G06F 9/38	X
	BI JP2000347863	Dec. 15, 2000	Japan	G06F 9/38	X
<b>OTHER PRIOR ART (including author, title, date, pertinent page, etc.)</b>					
	<del>BJ Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169</del>				
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CW	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.			
	BN	Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.			
	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.			
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE	
	<del>BP 09/435,069</del>	<del>Davidson et al.</del>	<del>Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug</del>	<del>Nov. 4, 1999</del>	
	AT9-99-491				
	BQ 08/538,071	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995	
	AUT919950055US1				
DATE CONSIDERED		10/05/2006			
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		<b>FILING DATE</b> March 22, 2004		<b>GROUP ART UNIT</b> 2188	
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL		APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
	BR	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
	BS	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
	BT	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
	BU	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
	BV	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
	BW	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
	BX	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
	BY	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
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	CA	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
	CB	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
	CC	10/757,250 / AUS920030488US1	Levine et al.	Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for use in Monitoring Performance of a Computer Program	Jan. 14, 2004
	CD	10/757,192 / AUS920030543US1	DeWitt, Jr. et al.	Method and Apparatus for Providing Pre and Post Handlers for Recording Events	Jan. 14, 2004
✓	CE	10/757,192 / AUS920030548US1	DeWitt, Jr. et al.	Method and Apparatus for Determining Computer Program Flows Autonomically Using Hardware Assisted Thread Stack Tracking and Cataloged Symbolic Data	March 18, 2004
<b>DATE CONSIDERED</b> 10/05/2006			<b>EXAMINER</b> /Craig Walter/		
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<b>Form PTO-1449</b>		<b>ATTORNEY DOCKET NO.</b> AUS920040061US1		<b>SERIAL NO.</b> 10/806,576	
<b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>		<b>APPLICANT</b> Dimpsey et al.			
		<b>FILING DATE</b> March 22, 2004		<b>GROUP ART UNIT</b> 2188	
<b>RELATED PATENT APPLICATIONS</b>					
<b>EXAMINER INITIAL</b>		<b>APPLICATION NO./ ATTY. DOCKET NO.</b>	<b>APPLICANT</b>	<b>TITLE</b>	<b>FILING DATE</b>
	CF	10/757,227 / <del>AUS920000556US1</del>	DeWitt, Jr. et al.	Method and Apparatus for Autonomically Moving Cache Entries to Dedicated Storage when False Cache Line Sharing is Detected	Jan. 14, 2004
	CG	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information	Jan. 14, 2004
	CH	10/807,094 / AUS920040059US1	Dimpsey et al.	Method and Apparatus for Providing Hardware Assistance for Code Coverage	Mar. 22, 2004
	CI	10/808,716 / AUS920040060US1	Dimpsey et al.	Method and Apparatus for Autonomic Test Case Feedback Using Hardware Assistance for Code Coverage	Mar. 22, 2004
	CJ	10/806,633 / AUS920040062US1	Dimpsey et al.	Method and Apparatus for Providing Hardware Assistance for Data Access Coverage on Dynamically Allocated Data	Mar. 22, 2004
	CK	10/806,917 / AUS920040063US1	Dimpsey et al.	Method and Apparatus for Automatic Test Case Feedback Using Hardware Assistance for Data Coverage	Mar. 22, 2004
	CL	10/806,871 / AUS920040064US1	Dimpsey et al.	Method and Apparatus for Hardware Assistance for Prefetching Data	Mar. 22, 2004
	CM	10/806,866 / AUS920040065US1	Dimpsey et al.	Method and Apparatus for Prefetching Data from a Data Structure	Mar. 22, 2004
<b>DATE CONSIDERED</b>		10/05/2006		<b>EXAMINER</b> /Craig Walter/	
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Substitute for form 1449/PTO		Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Application Number	10/806,576
		Filing Date	03-22-2004
		First Named Inventor	Dimpsey et al.
		Examiner Name	David T. Masdon
		Art Unit	2188
		Attorney Docket No.	AUS920040061US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
CW	AA 5394529	02-28-1995	Brown, III et al.	
	AB 6233679 B1	05-15-2001	Holmberg	
	AC 5887159	03-23-1999	Burrows	
	AD 6662295 B2	12-09-2003	Yamaura	
	AE 5938760	08-17-1999	Levine et al.	
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	AK 6647301 B2	11-11-2003	Sederlund et al.	
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CW	AO 2005/0108483 A1	05-19-2005	Bungo	

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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

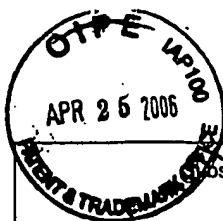
#### NON PATENT LITERATURE

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CW	AP	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.		

Examiner: /Craig Walter/

Date Considered: 10/05/2006

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Application Number	10/806,576
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Padmanabhan, Mano
Art Unit	2188
Attorney Docket No.	AUS920040061US1

Examiner Initials		Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
CW	AA	5,797,019	08-18-1998	Levine et al.	
	AB	6,549,998 B1	04-15-2003	Pekarich et al.	
	AC	2003/0101367 A1	05-29-2003	Bartfai et al.	
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CW	AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>		
CW	AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, <a href="http://ip.com">ip.com</a> , IPCOM000012728D, pp. 1-2		
CW	AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html</a>		

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10806576	
	Filing Date		2004-03-22	
	First Named Inventor	Dimpsey et al.		
	Art Unit	2188		
	Examiner Name	Masdon, David T.		
	Attorney Docket Number	AUS920040061US1		

## U.S. PATENTS

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
CW	1	6973417	B1	2005-12-06	Maxwell et al.	
CW	2	6721875	B1	2004-04-13	McCormick et al.	

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Application Number	10806576
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Examiner Name	Masdon, David T.
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CW	1	5966538		1999-10-12	Granston et al.	
CW	2	5940618		1999-08-17	Blandy et al.	
CW	3	6189142	B1	2001-02-13	Johnston et al.	
CW	4	5768500		1998-06-16	Agrawal et al.	
CW	5	6026235		2000-02-15	Shaughnessy	
CW	6	5950003		1999-09-07	Kaneshiro et al.	
CW	7	6594820	B1	2003-07-15	Ungar	

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Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
CW	1	20020073406	A1	2002-06-13	Gove	
CW	2	20030066055	A1	2003-04-03	Spivey	
CW	3	20030131343	A1	2002-07-10	French et al.	
CW	4	20040268316	A1	2004-12-30	Fisher et al.	

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